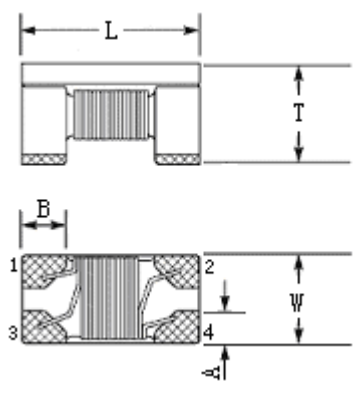


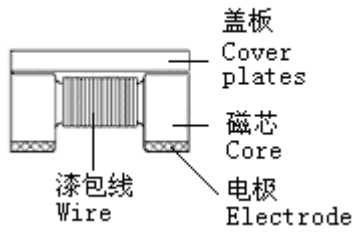
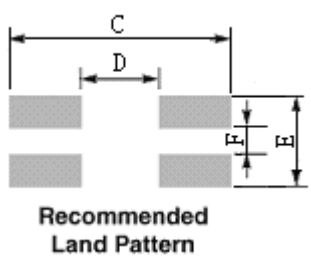
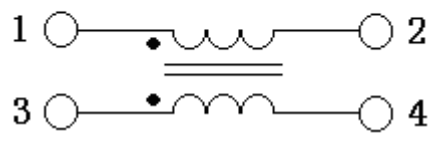




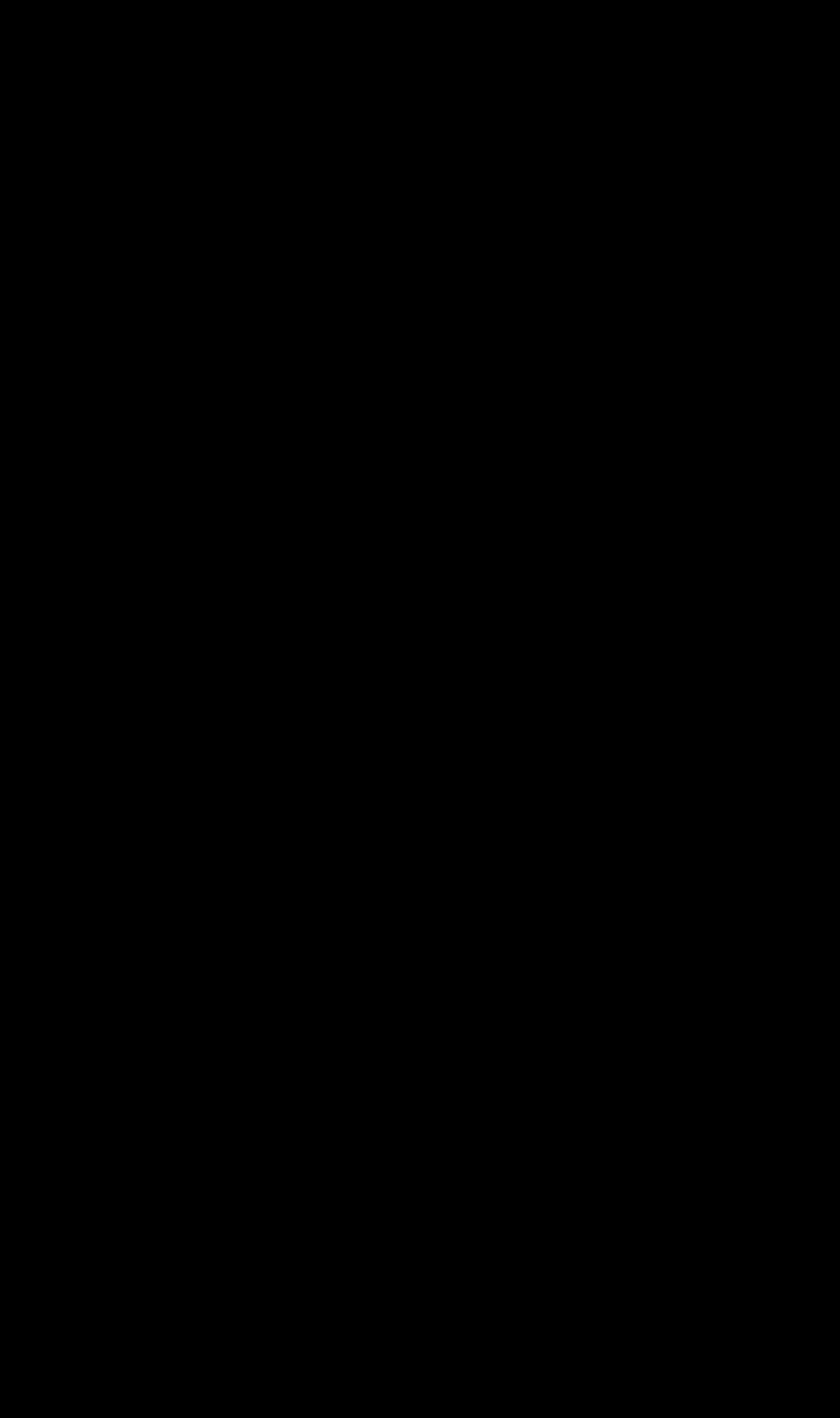
# 1 Dimension & Inner-configuration



Equivalent Circuit



No. Com460tent





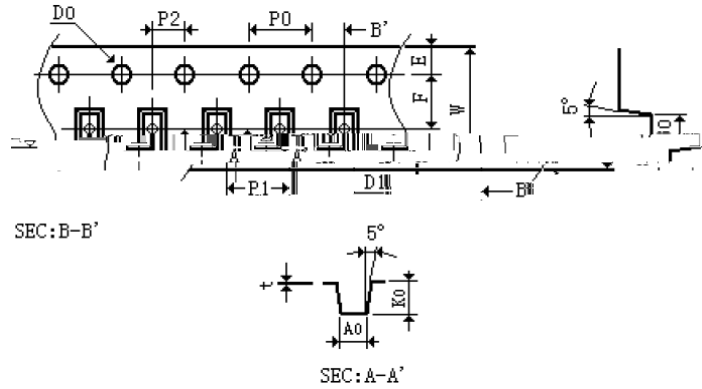






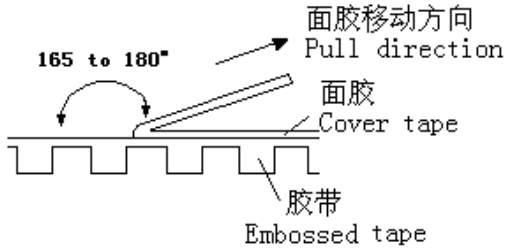


4 HJd] b[ `X] aYbg] cbg` fl b] h. `aaL`  
9aVcggYX` hLdY`



DJfh` BC`	K`	9`	:`	8\$`	8%`	D\$`	D%`	D&`	D\$y` %\$`	h`	5\$`	6\$`	?\$`
\$, \$)`	, "\$\$\$`	%+)`	' "\$)`	%)\$`	\$"*`	("\$\$`	("\$\$`	&"\$\$`	("\$\$`	\$"&`	%)\$`	&"&`	%'(\$`
%&\$*`	, "\$\$\$`	%+)`	' "\$)`	%)\$`	\$"*`	("\$\$`	("\$\$`	&"\$\$`	("\$\$`	\$"&`	%-\$`	' "\$)`	&"\$\$`

5 ` `DY` ] b[ `cZZ` ZcfW`



**Peeling required**

\$, \$) %&\$\*` &\$` , \$` ..... \$, \$) %&\$\*` gYf] Yg` &\$[ , \$[ .....`

`HYgh` WtbX] h] cb`

' \$\$aa#a] bw %\$i` ..... GdYX` cZ` dY` ] b[ `cZZ` .` \$\$aa#a] bw %\$i`

..... %\$)v` % \$v` ..... 5b[ `Y` cZ` dYY` ] b[ `cZZ` .` %\$)v` % \$v`

\* DUWU[ ] b[ `bi aYf` fl b] h. `DVg` L`

	G] nY`	%&\$*`	\$, \$)`
	DYf` FY`	&\$\$\$\$`	' \$\$\$`
DYf` 6cl`	'`	*\$\$\$\$`	- \$\$\$`
	)`	%\$\$\$\$`	% \$\$\$`
	%\$`	&\$\$\$\$`	' \$\$\$`
DYf` 7UgY`	%)`	' \$\$\$`	( ) \$\$\$`
	'`	*\$\$\$\$`	- \$\$\$`
	(`	, \$\$\$`	%&\$\$\$\$`
	)`	%&\$\$\$\$`	% \$\$\$`

